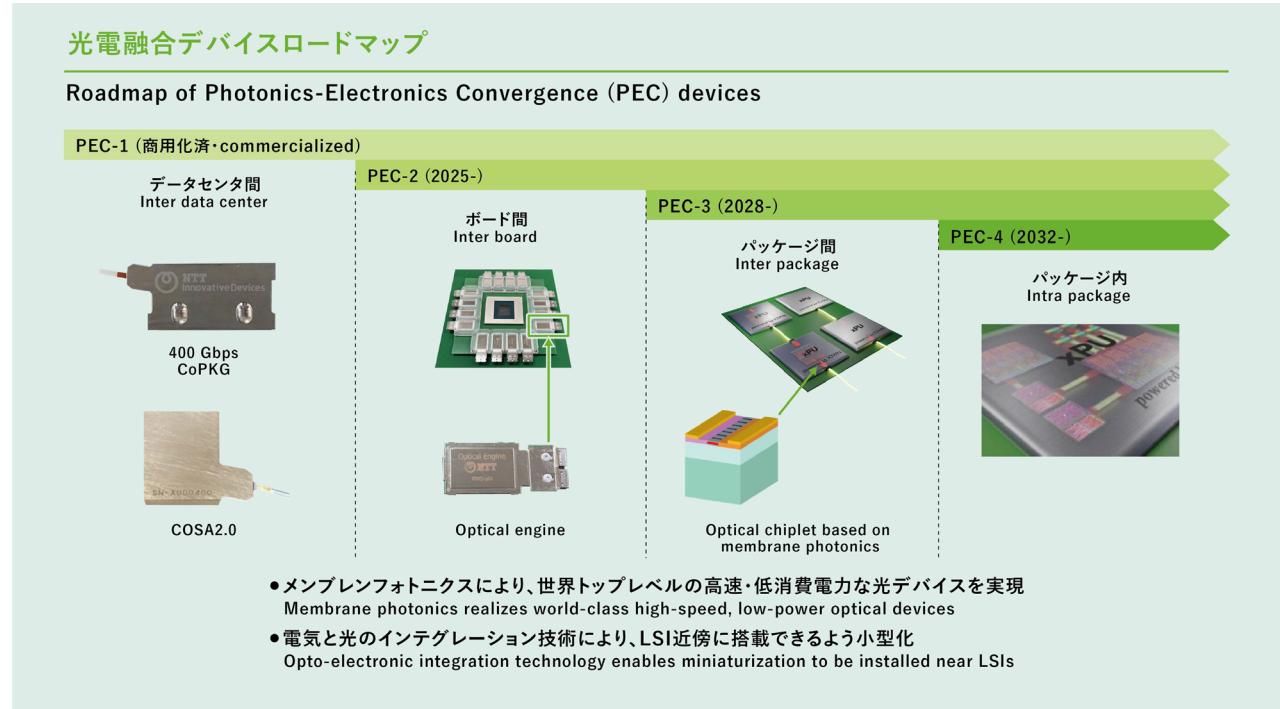


Reduction in power consumption Photonics-electronics convergence device

Background and Technical Challenges

The number of LSIs required for computing is increasing explosively, but there is a limit to communication distance with electrical wiring alone, making it impossible to connect many LSIs.



R&D Goals and Outcomes

By connecting LSIs with optical signals, we support the development of optical disaggregated computers.

Key Technologies

01 Core Technologies

Our unique technology of membrane photonics and technology for integrating electrical chips and optical fibers

02 Key Differentiators

Membrane photonics enables us to realize the world's highest level of high speed and low power consumption optical devices. Integration technology enables compact devices.

Use Cases Information Technology (IT)

R&D phase Development

Technology Schedule FY27-29 (PEC-3)

Commercialization Schedule FY27-29 (PEC-3)

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NTT Device Innovation Center

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[Contact]
Optical Interconnect Device Project

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